Post Lab 5

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<https://github.com/u1252010/ECE-6780-u1252010>

1. The AUTOEND bit causes the master to send a stop condition immediately upon NBYTES transfer completion. You do not want to use it when you want to send a restart condition as you can’t, the stop condition will be sent before you can send the restart condition.
2. I would write the following:
   1. I2C2->TIMINGR |= (0<<28);
   2. I2C2->TIMINGR |= (0x9<<0);
   3. I2C2->TIMINGR |= (0x3<<8);
   4. I2C2->TIMINGR |= (0x1<<16);
   5. I2C2->TIMINGR |= (0x3<<20);
3. The enables are the following bits in the I2C\_CR1 register:
   1. TCIE – bit 6
   2. NACKIE – bit 4
   3. TXIE – bit 1
   4. ERRIE – bit 7
4. The datasheet says the following:
   1. SDO = 1 : SAD = 1101011b
   2. SDO = 0 : SAD = 1101010b